



General Discription

The SET5808 device is a high-performance, low-cost, single-chip, stereo analog-to-digital converter with single-ended analog voltage input. The SET5808 device uses a delta-sigma modulator with 64-times oversampling and includes a digital decimation filter and high-pass filter that removes the dc component of the input signal.

For various applications, the SET5808 device supports master and slave mode and two data formats in serial audio interface.

Fabrication of the SET5808 device uses a highly advanced CMOS process. The device is available in a small, 14-Pin TSSOP package.

Features

- Stereo Δ - Σ ADC
- On-chip digital anti-alias filter
- Single-ended input
- Digital high-pass filter to eliminate DC offset
- S/(N+D): 87dB@3.3V for 48KHz
- DR: 101dB@3.3V for 48KHz
99dB@3.3V for 96KHz
- S/N: 101dB@3.3V for 48KHz
99dB@3.3V for 96KHz
- Sampling Rate: 8KHz~96KHz
- System Clock:
256fs/384fs/512fs
- CMOS Input Level
- Master/Slave mode
- Audio Interface: Left-justified, 24-bit/I2S, 24-bit
- Power Supply: 2.7 to 3.6V
- Ta= -20 ~ 85°C (commercial)
- Package: 14-pin TSSOP